

In Place of FORM PTO-1449 (Modified)

Serial No.:

Applicant: Sean C. Erickson, et al.

Filing Date: July 25, 2003

Group:

Att. Docket No.: 03-0805

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT**

Reference Designation

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
✓ AAA	6,544,829	04/08/03	Gopinath et al.	438	232	09/20/02
ABA	6,269,472	07/31/01	Garza et al.	716	21	12/12/97
ACA	5,705,301	01/06/98	Garza et al.	430	5	02/27/96
ADA						
AEA						
AFA						
AGA						
AHA						
AIA						
AJA						

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
AKA						
ALA						
AMA						
ANA						
AOA						

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner

Initial

✓ APA Weste, Neil et al.; Principles of CMOS VLSI Design; Addison-Wesley Pub. Co.; October, 1994; 113-116, 132-133

✓ AQA Sze; VLSI Technology; McGraw-Hill; 1988; pgs. 416-417

ARA

Examiner:

*Vidhi T...*

Date Considered:

*7/7/04*

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.